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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2816  
Examiner: Shareill Coles

In Re PATENT APPLICATION Of:

Applicant(s) : Hiroki GOKO

Serial No. : 10/649,931

Filed : August 28, 2003

For : SEMICONDUCTOR INTEGRATED  
CIRCUIT

Attorney Ref: OKI 369

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) **STATUS REQUEST**  
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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Please let us know the status of the above-identified application and when an Action may be expected. Please respond via facsimile or e-mail Tonya Harris (Docketing Manager) at [tonya@rabinchamp.com](mailto:tonya@rabinchamp.com).

Respectfully submitted,

*Steven M. Rabin*

June 22, 2004  
Date

SMR:dt

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